

CPU CORE
Page:23

VCC_CORE

3.3V/5V/12V
5VPCU/3V_ALWAYS
3V_S5/5V_S5
3VSUS/5VSUS
Page:24

+12V
5VPCU
3VPCU
+3.3V
+5V
3V_S5
5V_S5
3VSUS
5VSUS

2.5VSUS/+2.5VTT
MVREF_DM
SMDDR_VTERM
Page:25

2.5VSUS
+2.5V
VTT
MVREF_DM
SMDDR_VTERM

+1.8V/1.8V_S5
Page:25

1.8V_S5
+1.8V

BATTERY CHARGER
Page:26

CLOCK GEN
ICS
952023CG
+3.3V Page:13

DDR CLOCK
BUFFER
ICS97322
+2.5V Page:13

ZL6
CPU
CELERON-M
INTEL Mobile_479 CPU
VCC_CORE
VTT Page:2,3

PCI ROUTING TABLE	IDSEL	INTERUPT	DEVICE
REQ0# / GNT0#	AD22	INTB# , INTC#	MINI-PCI
REQ1# / GNT1#	AD17	INTD#	TI 1410A

DDR-SODIMM1
Page:11,12

DDR-SODIMM2
Page:11,12

+2.5V
+1.25V

IDE-HDD
+5V Page:18

IDE-ODD
+5V Page:18

ATA 66/100
ATA 66/100

AC97

Reltek
+5V Page:19
+3.3V

AMP
MAX9755
+5V Page:20

MODEM
MDC 1.5
+3.3V Page:19

MIC IN
SPEAKER
LINE OUT
RJ11

SB
SiS 963L
Page:8,9,10

NS
KBC(97551)
+3.3V Page:21

Touchpad
+5V
Keyboard
FLASH
+3.3V
FAN
+5V

NB
SiS M661MX
Page:4,5,6,7

HOST BUS 400MHZ

HUB I/F 66(266)MHZ

PCI BUS 33MHZ

MII PHY

USB 2.0

LPC 33MHZ

SiS 302ELV
Page:14

LVDS
Page:14

CRT
Page:6

ENE
PCMCIA
CB1410
+5V
+3.3V Page:16

MINI-PCI
Wireless LAN
+3.3V Page:17

REALTEK
10/100 LAN
RTL8201CP
+3.3V Page:15

TRANSFORMER
NS0013
Page:15

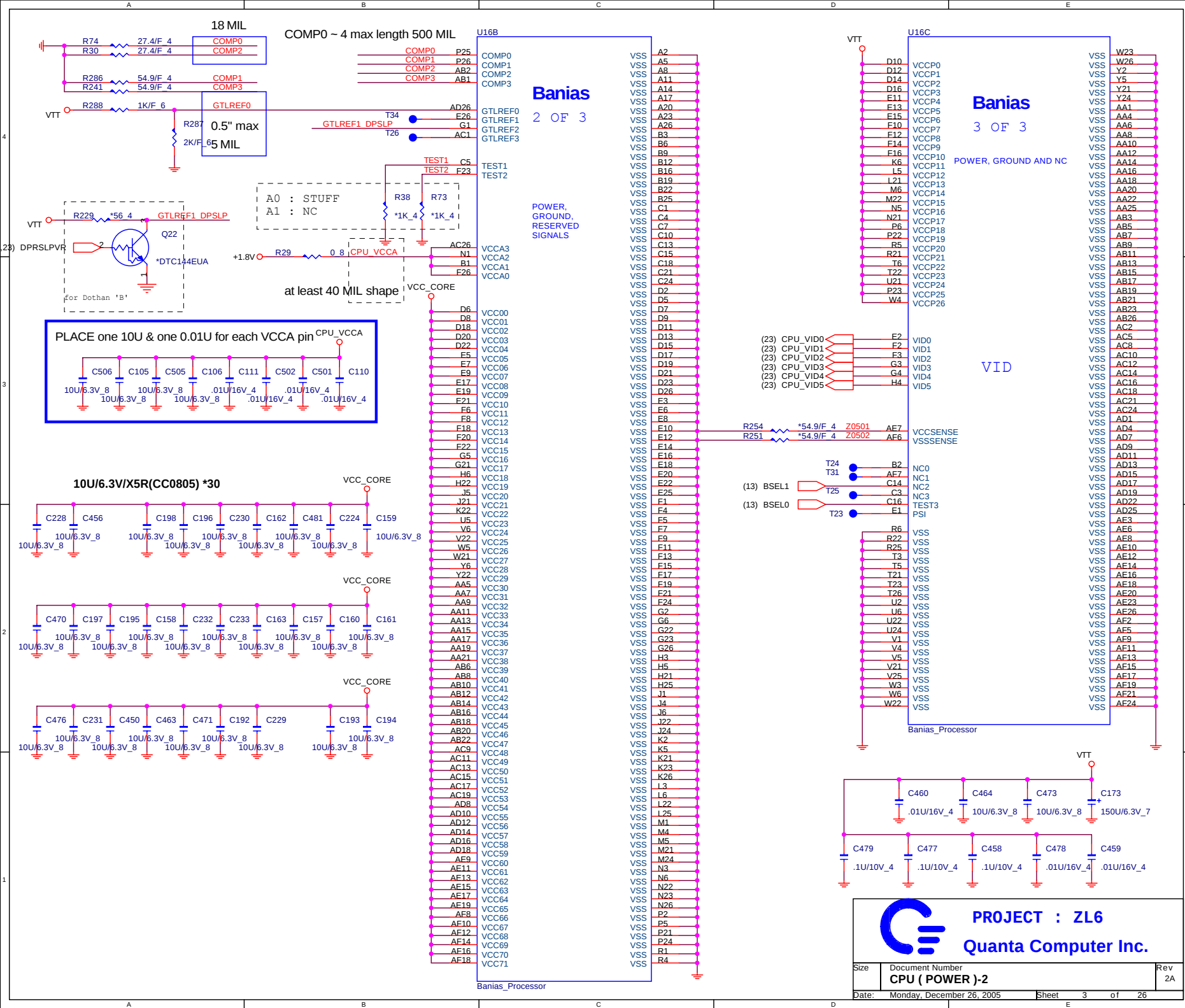
RJ45

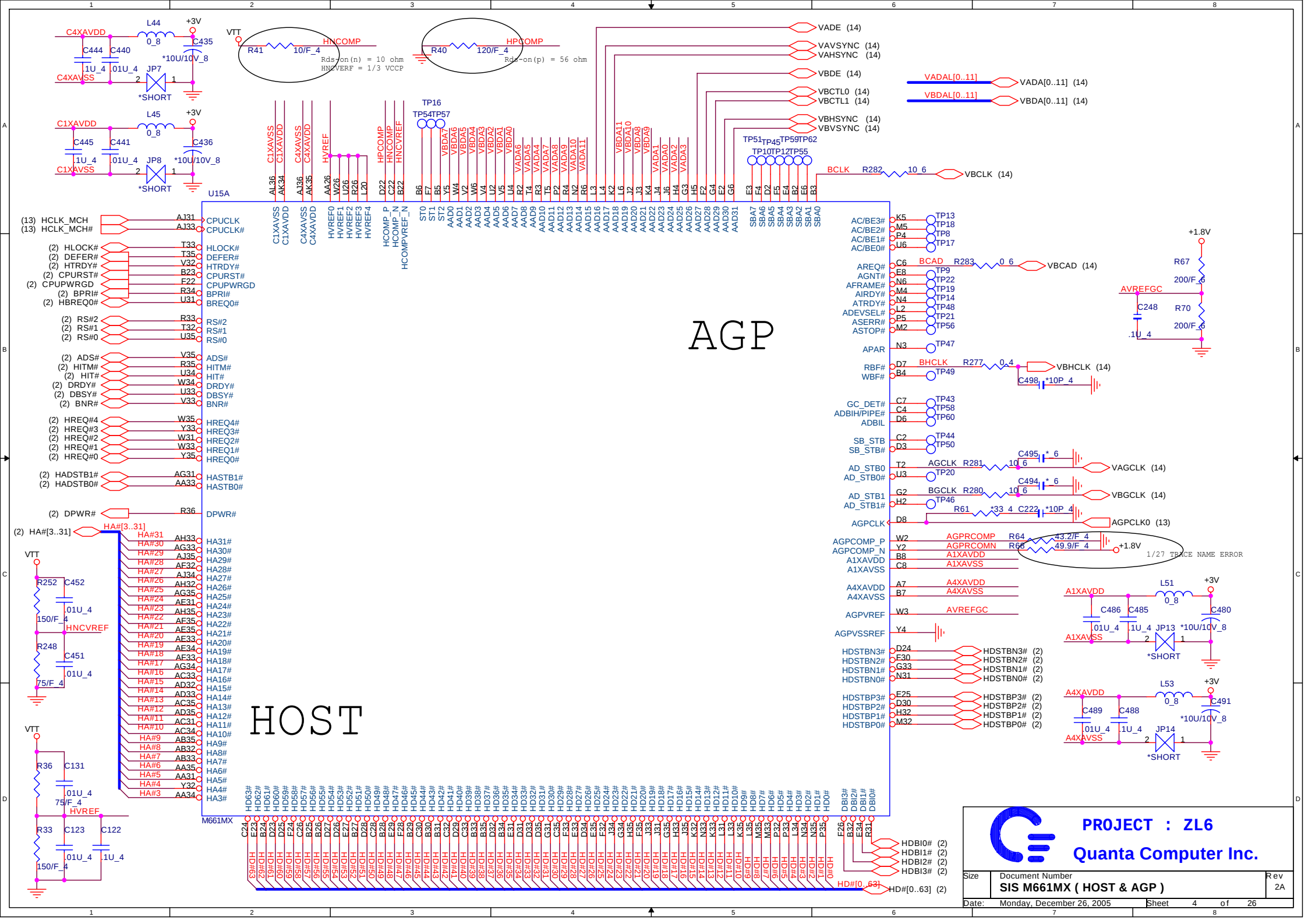
SYSTEM 3
USB PORT
5VSUS
Page:17
USB1,2,3

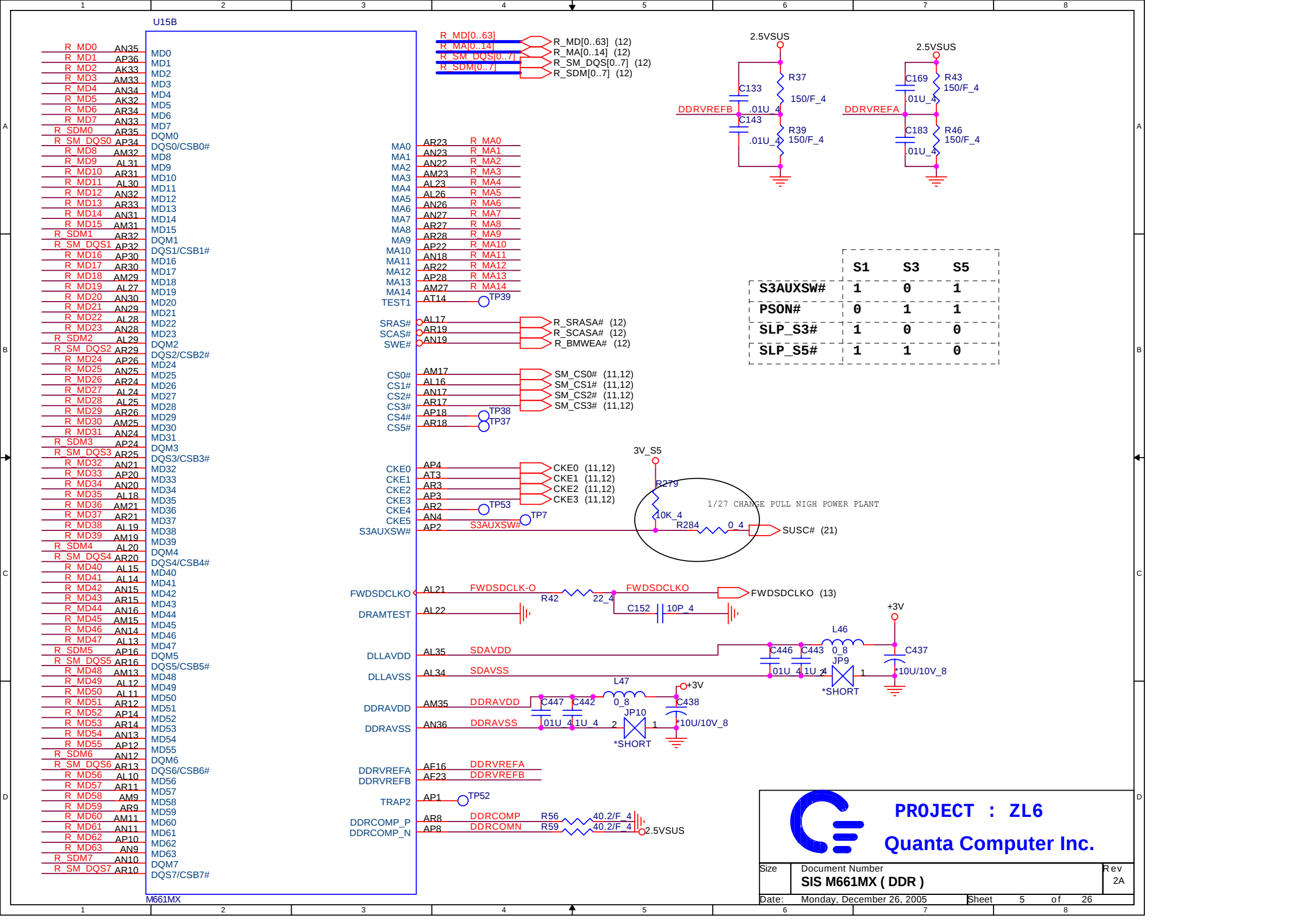
MINI-USB
3VSUS
Page:17
USB4

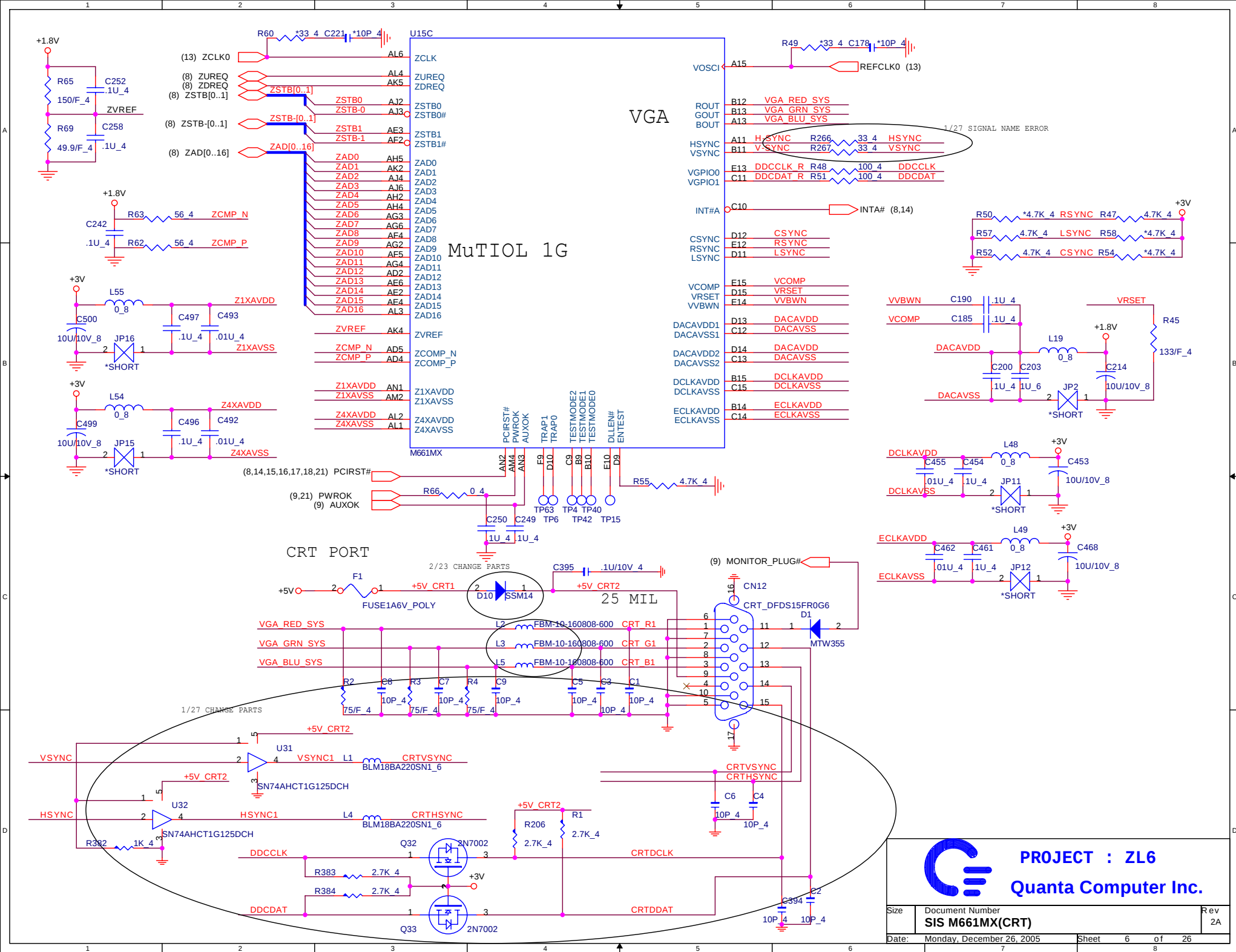


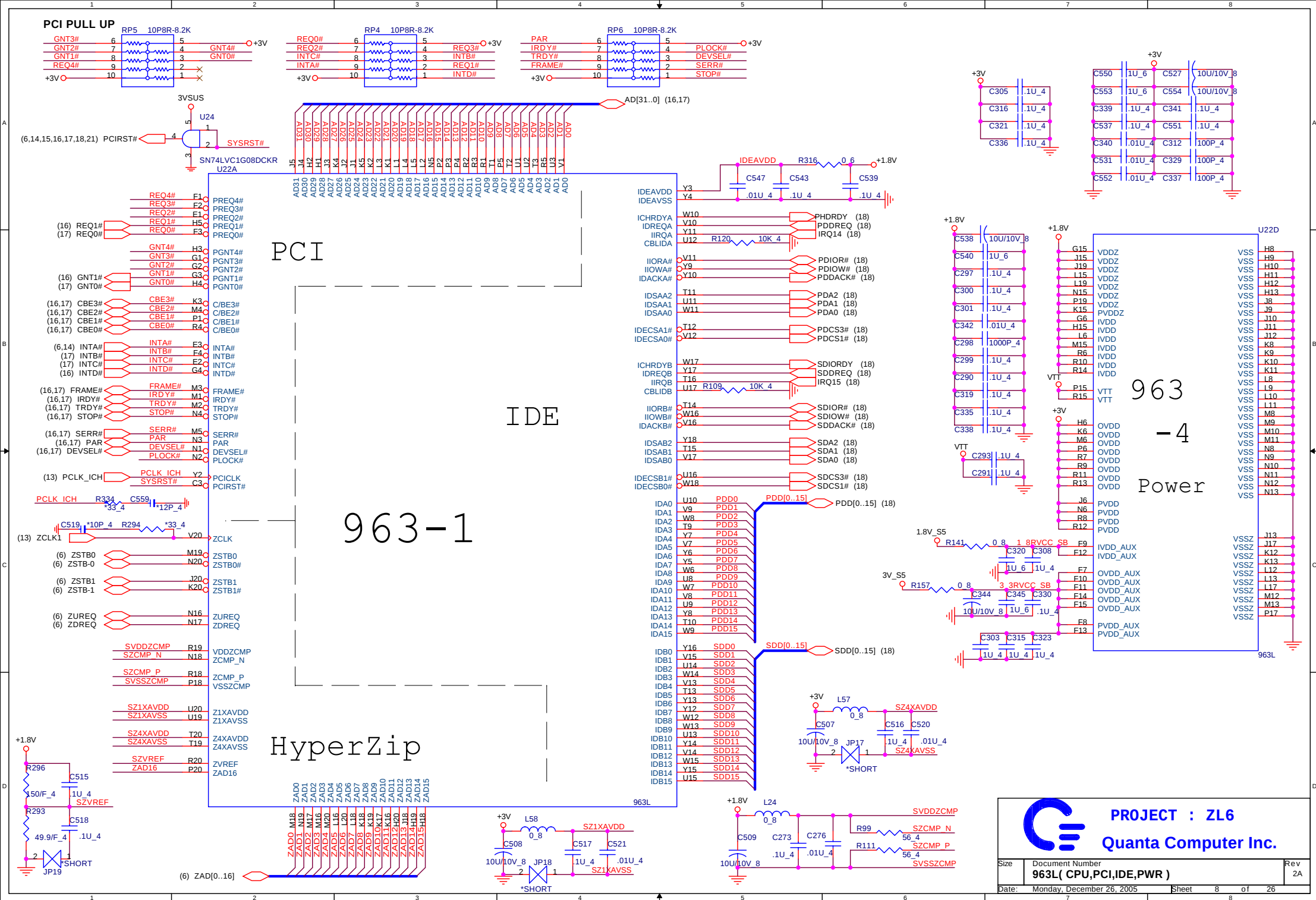
PROJECT : ZL6
Quanta Computer Inc.

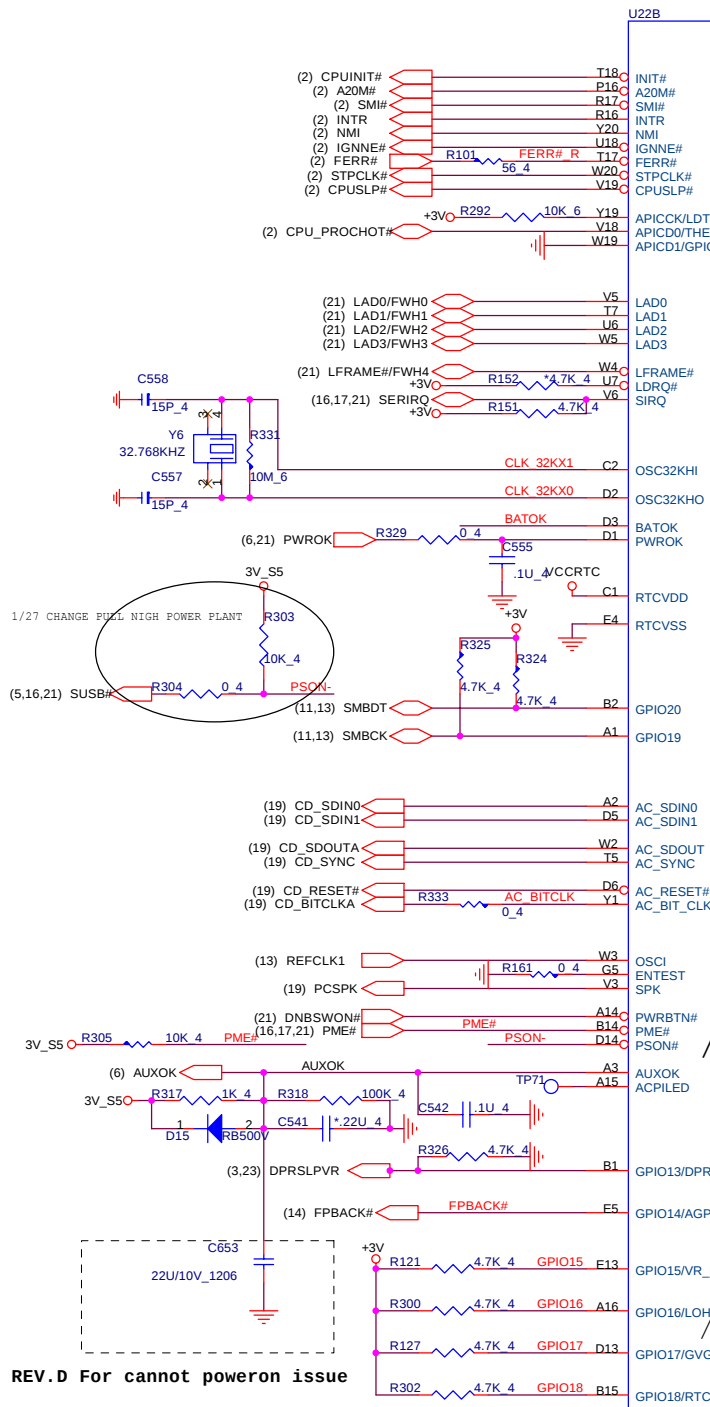












CPU_S

APIC

LPC

RTC

963-2

GPIO

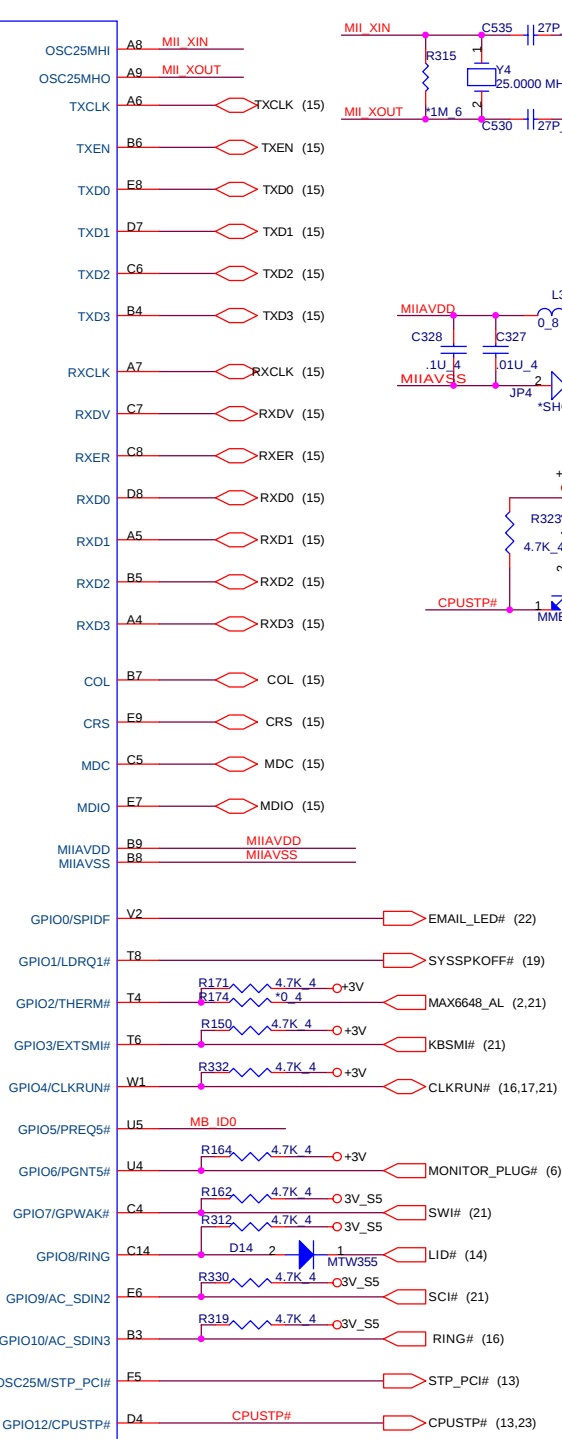
AC97

ACPI
/others

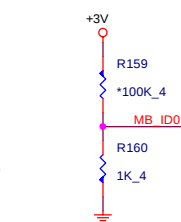
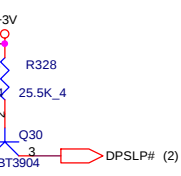
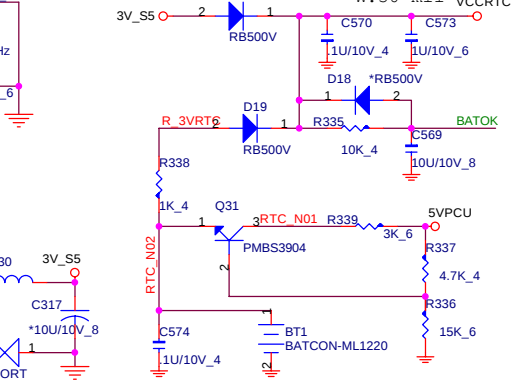
KBC
/geyserville

MII

GPIO



RTC





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Size	Document Number	Rev
	963L (HOST,LPC,MII,GPIO,RTC)	2A
Date:	Monday, December 26, 2005	Sheet 9 of 26

C354*10P 4 R165 *33_4
(13) CLK48_USB

TP70 B18 UV0+
TP24 C18 UV0-
TP23 D18 UV1+
TP67 D19 UV1-
E14 UV2+
D15 UV2-
E18 UV3+
F18 UV3-
E16 UV4+
E15 UV4-
G18 UV5+
G19 UV5-

USBOC0# G20 OC0#
USBOC1# G17 OC1#
USBOC2# J16 OC2#
USBOC3# H16 OC3#
USBOC4# H17 OC4#
USBOC5# G16 OC5#

3VSUS L25 USBVDD
0_8
C277 10U/10V_8
C284 1U_6
C285 .1U_4
D16 USBVDD
D17 USBVDD
E17 USBVDD
F17 USBVDD
F19 USBVSS
E19 USBVSS
B19 USBVSS
B17 USBVSS

3VSUS
USBOC0# R90 10K 4
USBOC1# R92 10K 4
USBOC2# R88 10K 4
USBOC3# R87 10K 4
USBOC4# R91 10K 4
USBOC5# R89 10K 4

TP74 A12 D0
TP75 B12 D1
TP30 C12 D2
TP26 D12 D3
TP27 E12 D4
TP72 A13 D5
TP73 B13 D6
TP25 C13 D7
TP29 D11 CTL0
TP28 C11 CTL1

ACR-IPB
/ADSL
(For 962,963)

ACR-WIRELESS
/BLUETOOTH
(ONLY FOR 963)

963-3

OTHERS

USB

U22C

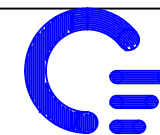
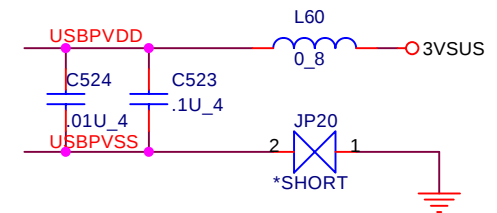
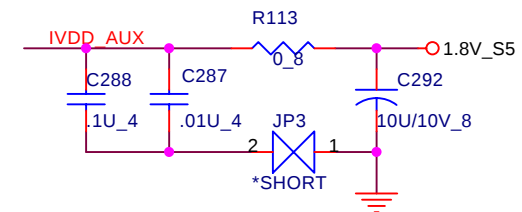
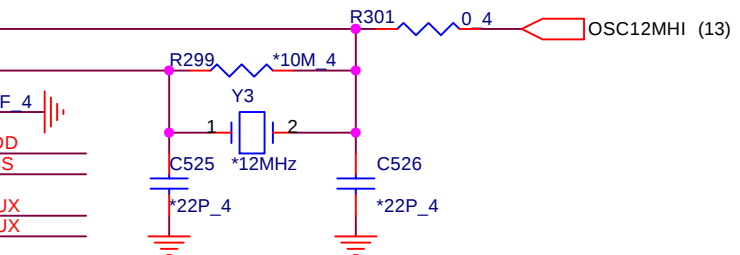
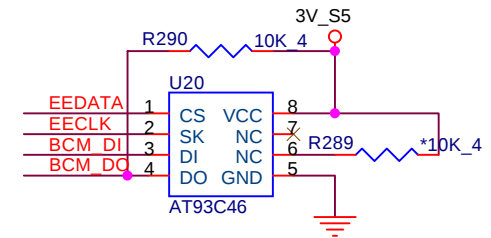
SCLK E11 TP31
LINKON C19 TP66
LREQ A19 TP69
LPS A20 TP68
E20 EECLK
D20 BCM_DI
E20 BCM_DO
C20 EEDATA
GPIO21/EESK
GPIO22/EEDI
GPIO23/EEDO
GPIO24/EECS

OSC12MHI B16 OSC12MHI R
OSC12MHO A17 OSC12MHO
F16 R115 412/F 4
USBREF
USBPVDD A18 USBPVDD
USBPVSS C15 USBPVSS
IVDD_AUX C16 IVDD_AUX
IVDD_AUX C17 IVDD_AUX

IPBRST# B11 TP76
TDFRAME D10 TP33
RDFRAME A11 TP77
IPB_RDCLK E10 TP34
IPB_TDCLK D9 TP36

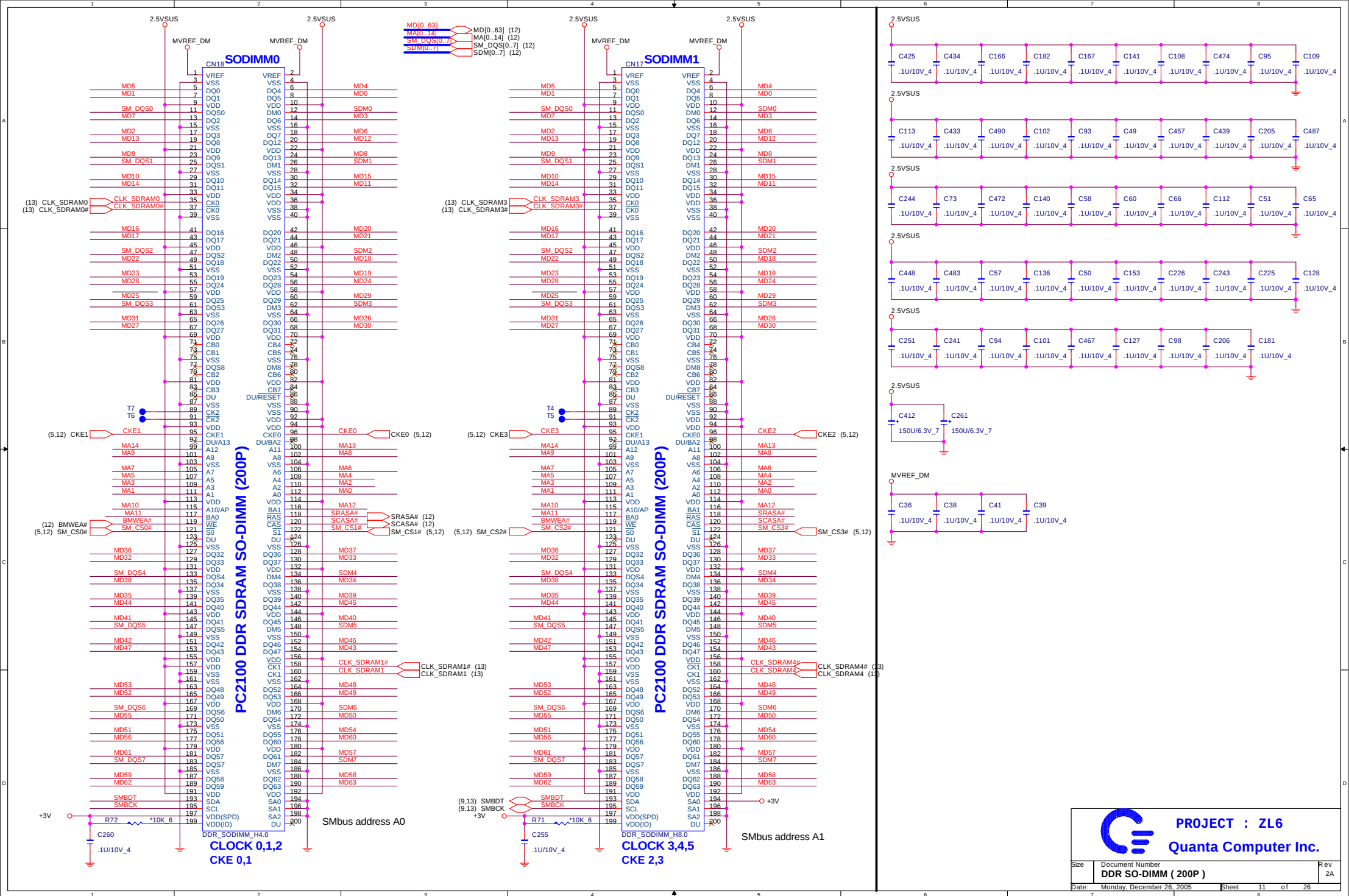
IPB_OUT0/PLENN B10 TP78
IPB_OUT1/ZCLKSEL A10 TP79
IPB_IN0 C10 TP32
IPB_IN1 C9 TP35
USBREFAVDD B20 USBPVDD

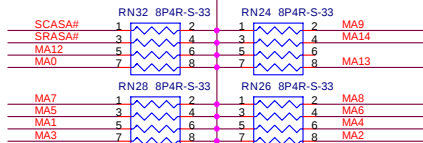
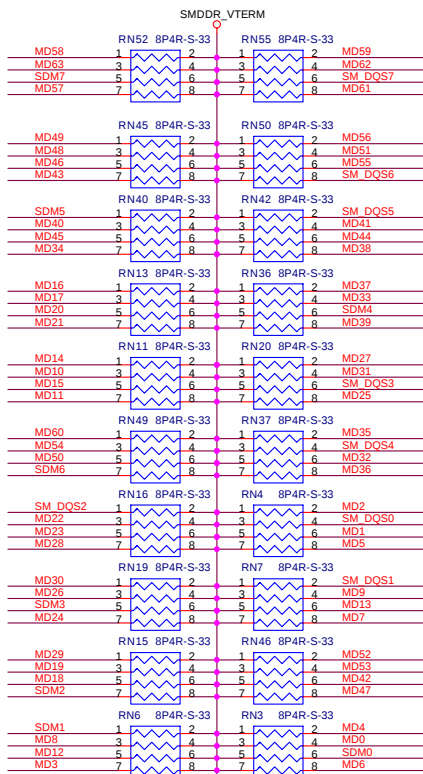
963L



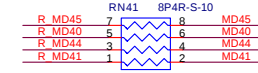
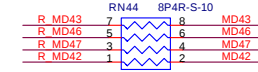
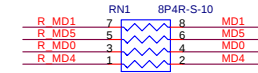
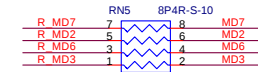
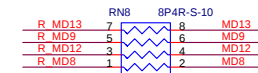
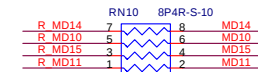
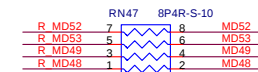
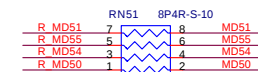
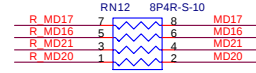
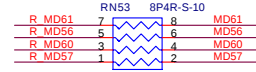
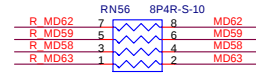
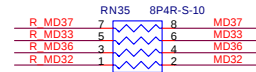
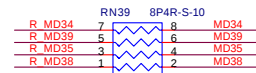
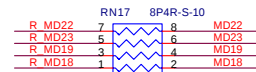
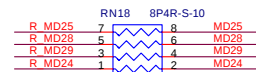
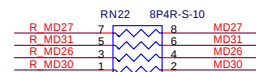
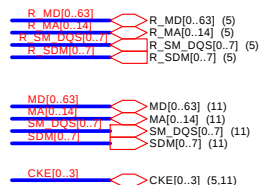
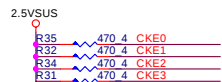
PROJECT : ZL6
Quanta Computer Inc.

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	963L (USB)	2A
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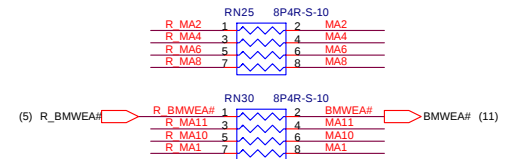
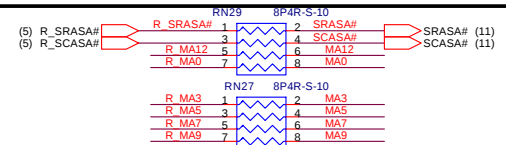
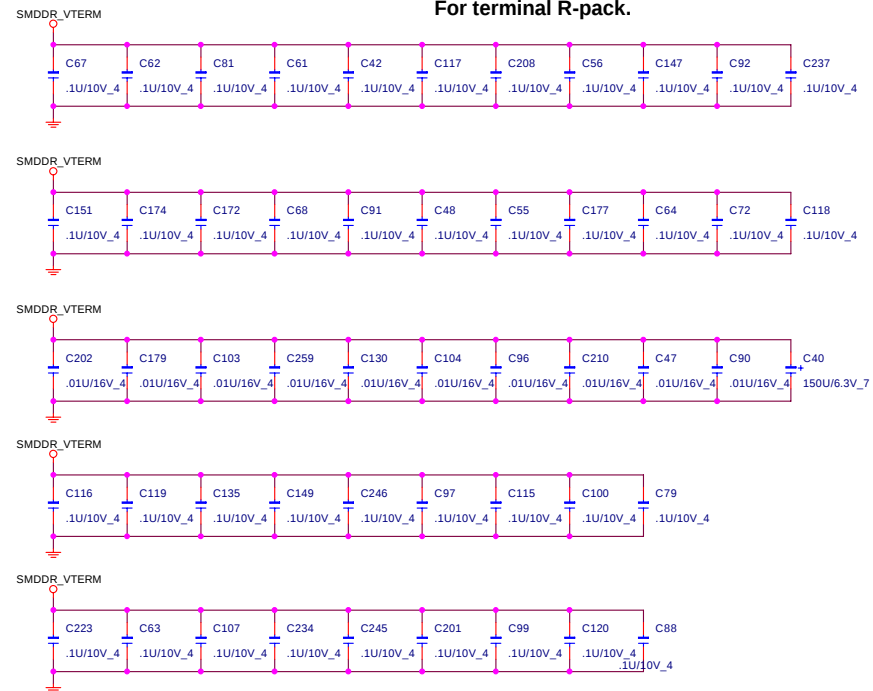




Damping put BOT, termination put TOP



For terminal R-pack.

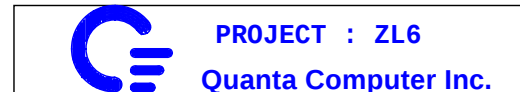


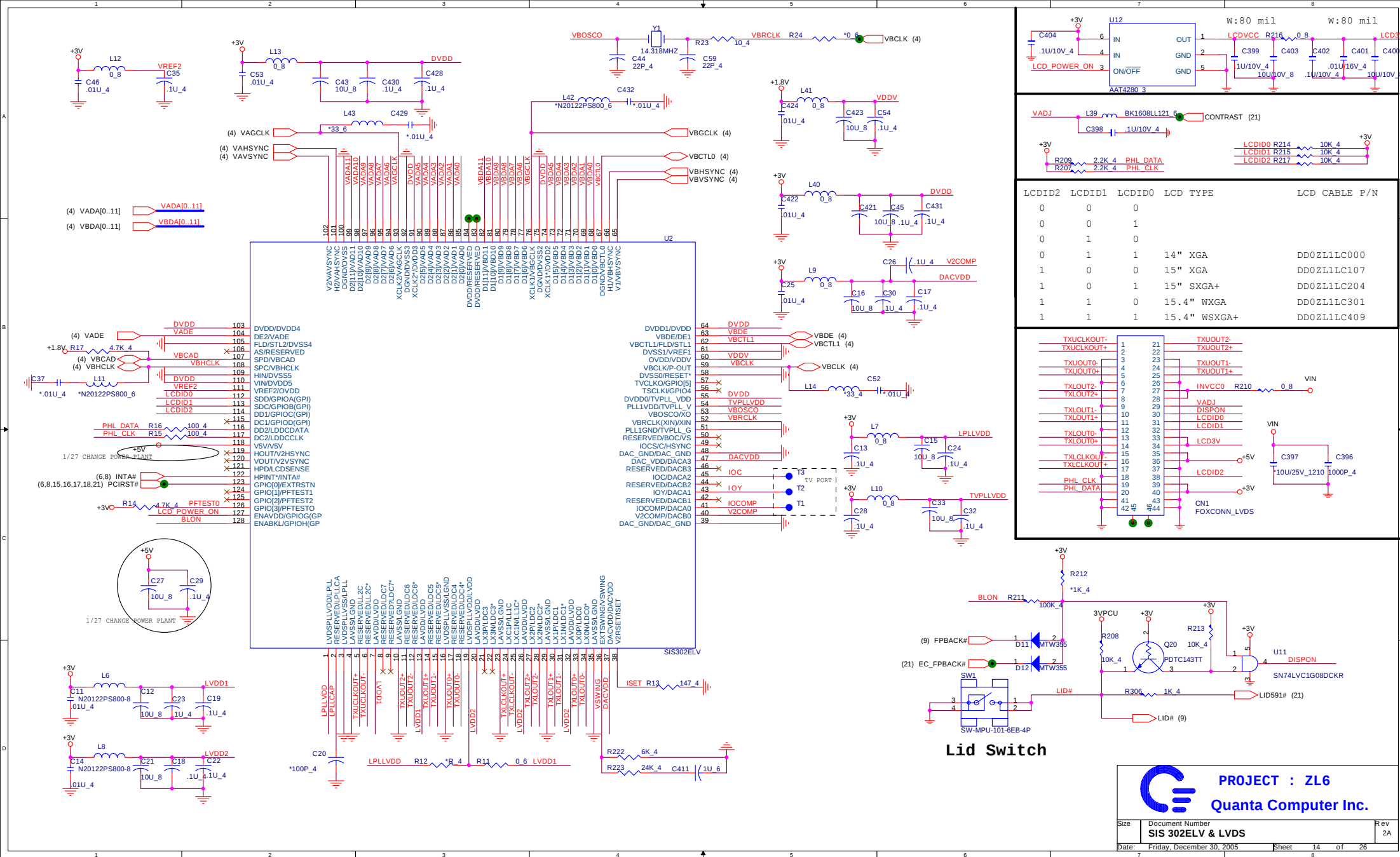
PROJECT : ZL6
Quanta Computer Inc.

```

Default:  FS4    FS3    FS2    FS1    FS0
           0      0      0      1      1

```

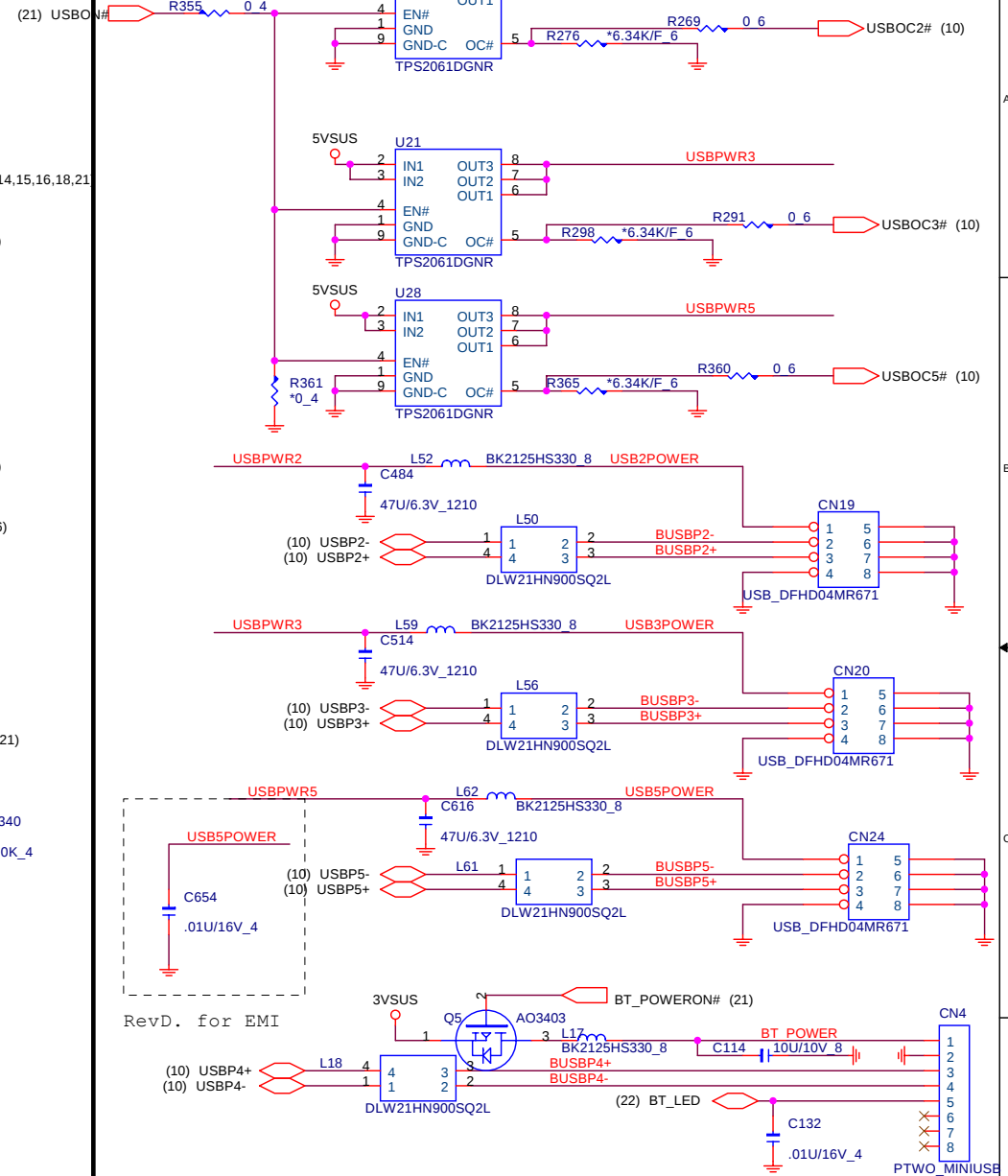
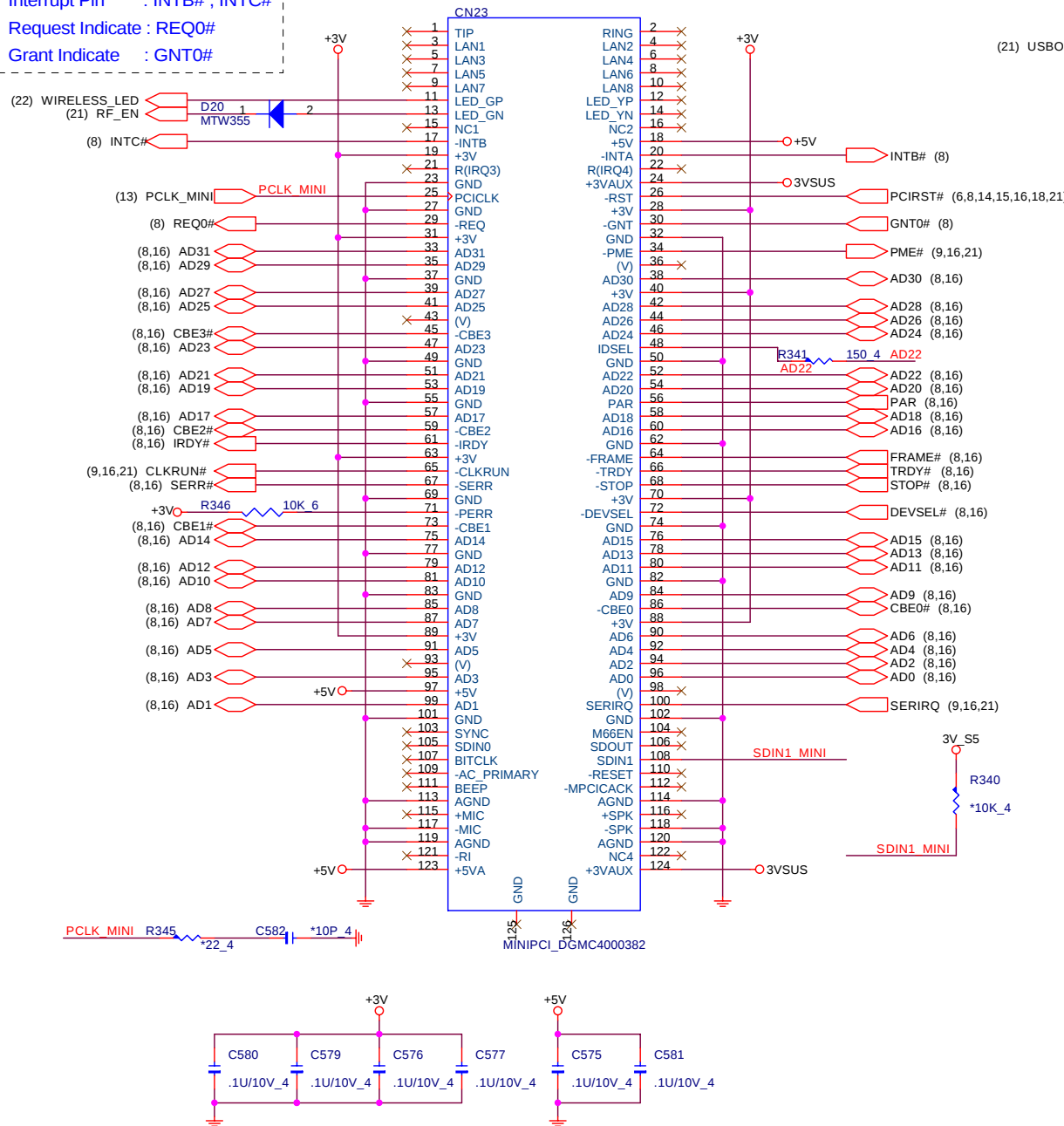


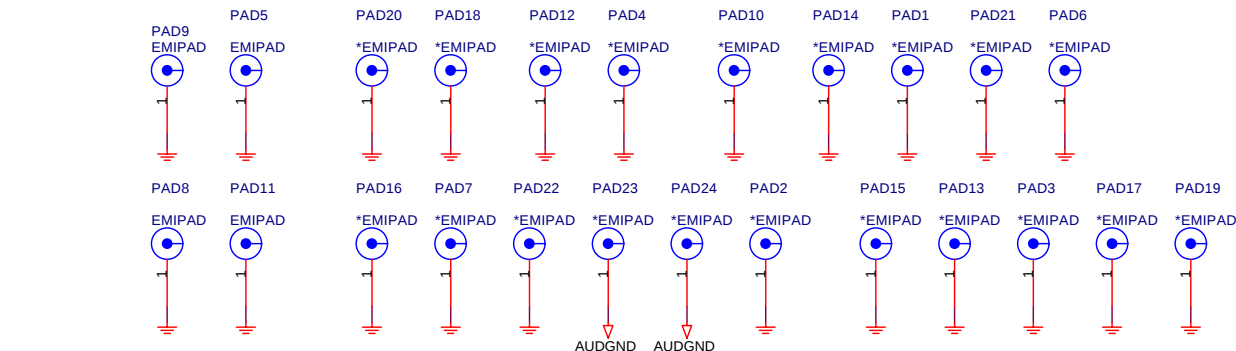
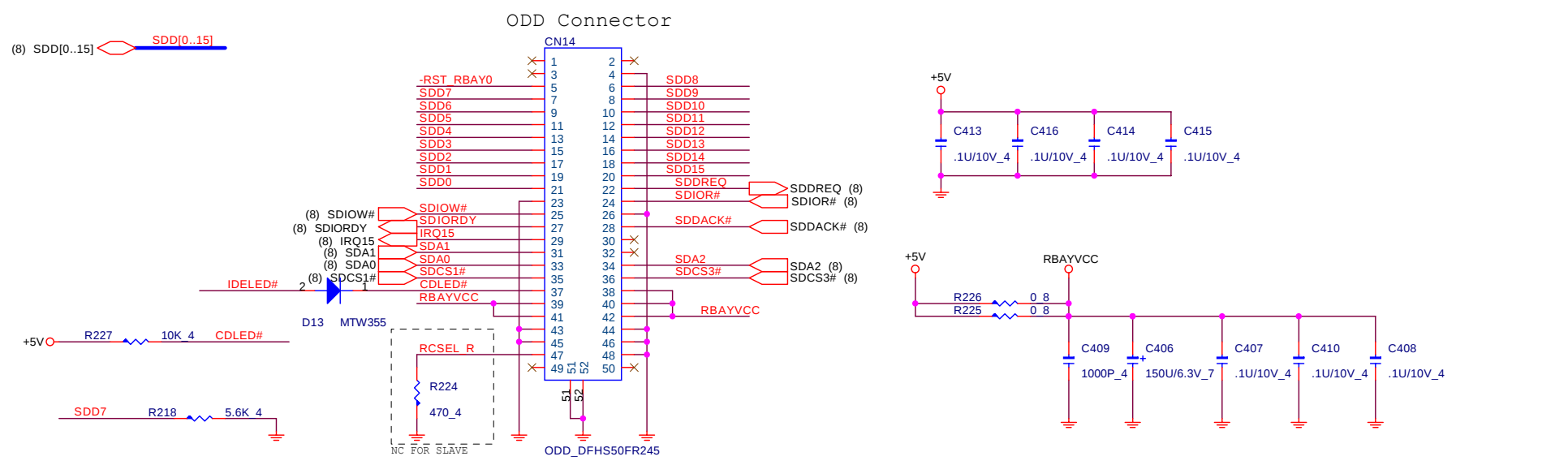
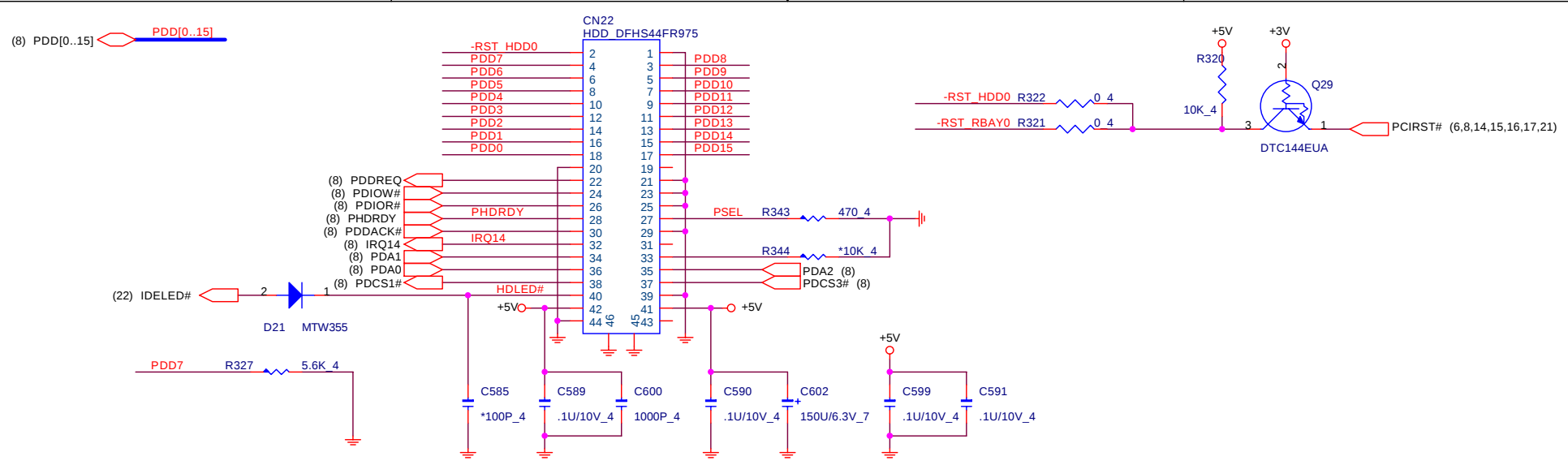


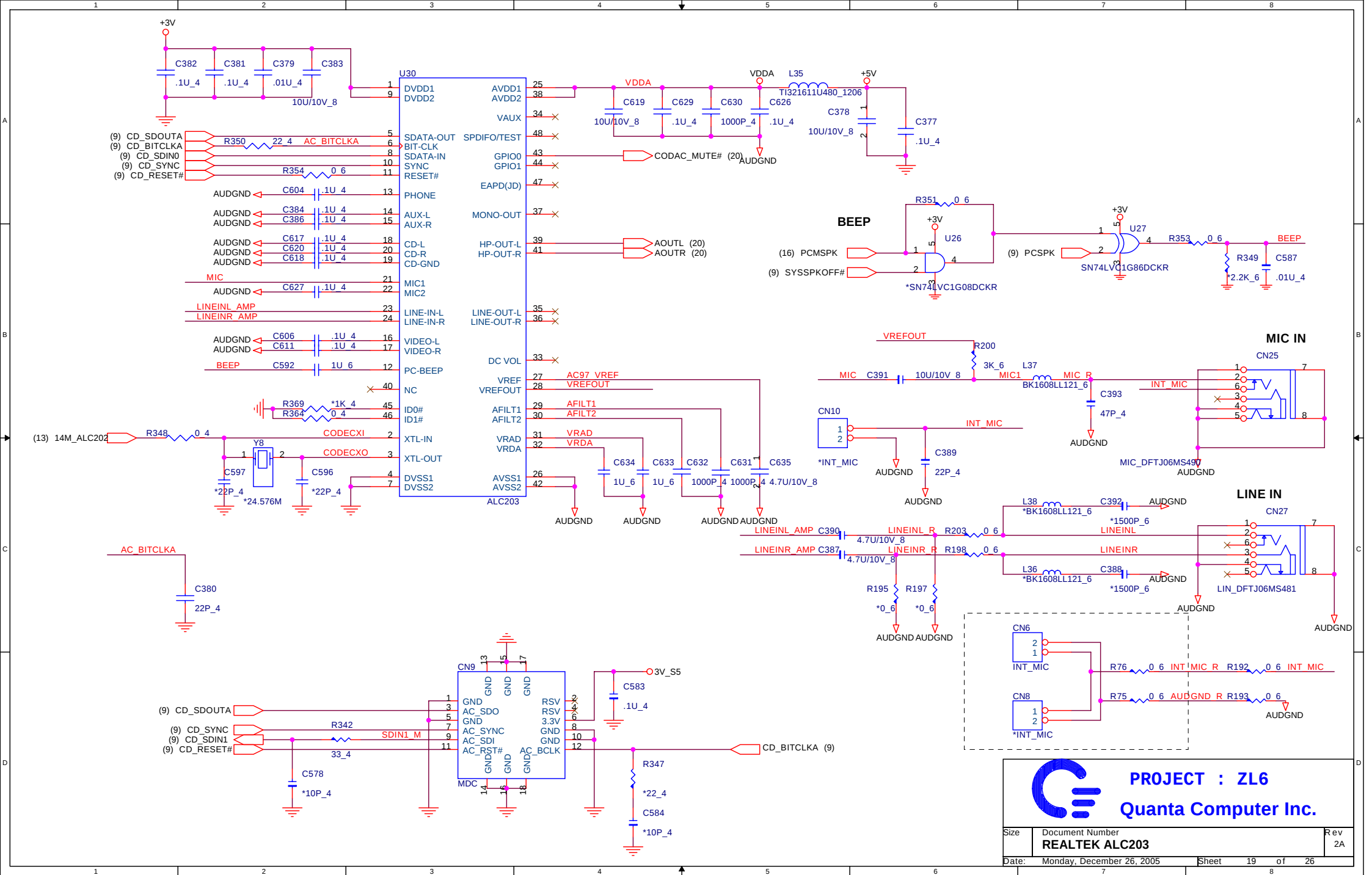
Size	Document Number PCMCIA(PCI1410)	Rev 2A
Date:	Monday, December 26, 2005	Sheet 16 of 26

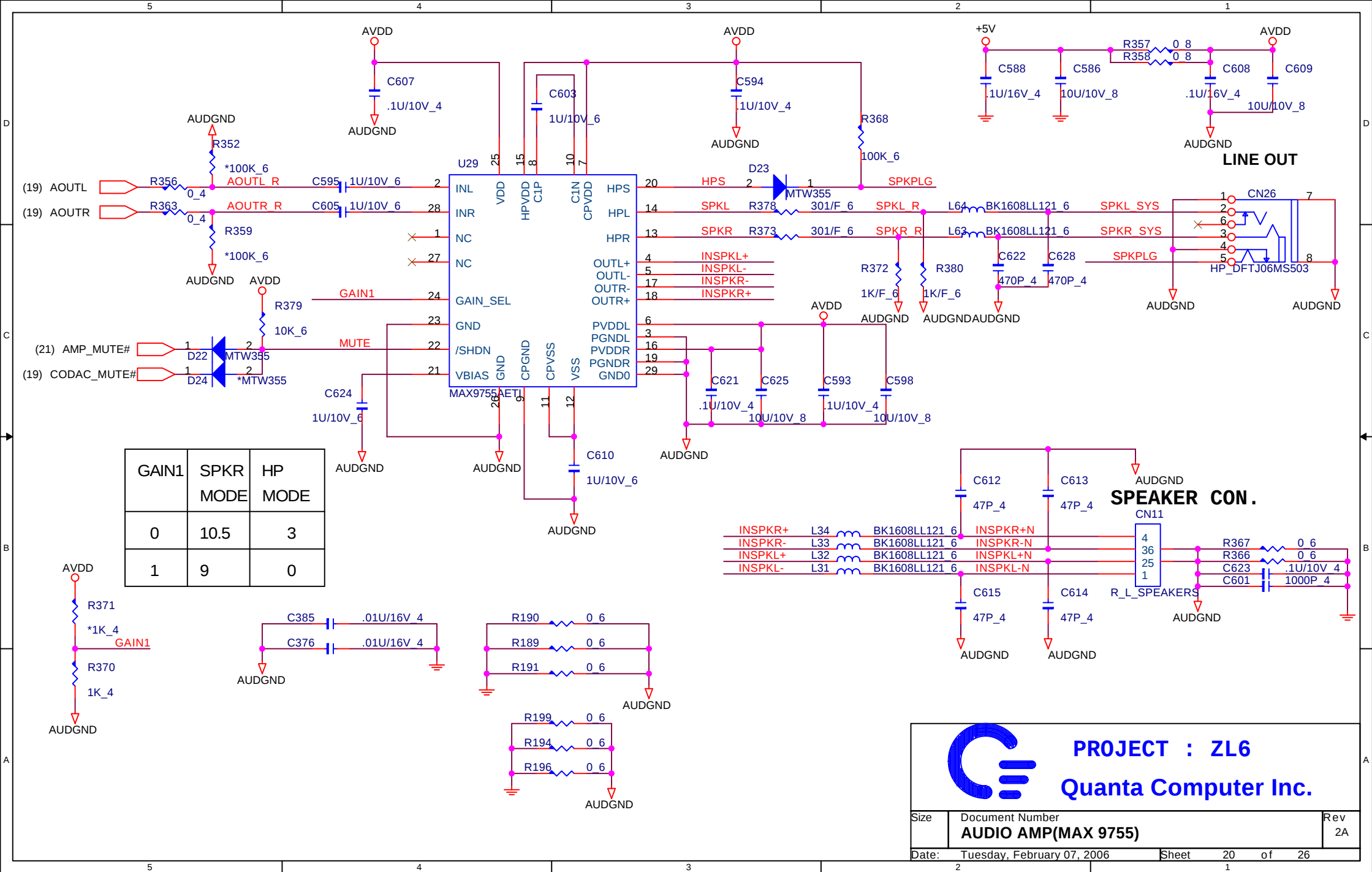
ID Select : AD22
Interrupt Pin : INTB# , INTC#
Request Indicate : REQ0#
Grant Indicate : GNT0#

MINI-PCI

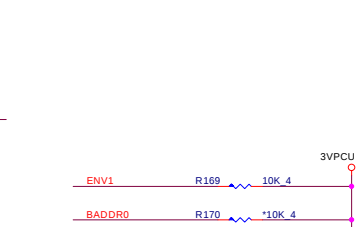
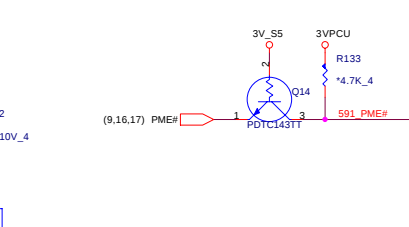
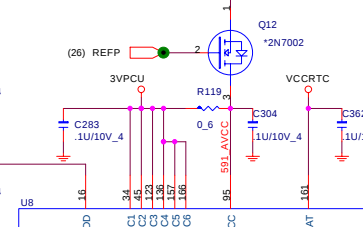
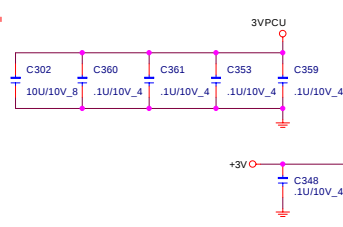
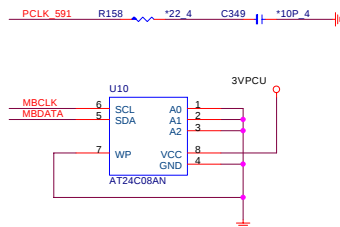






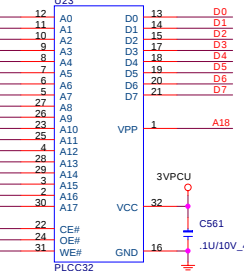
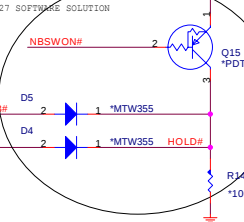
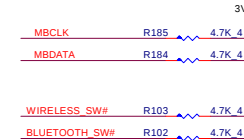


LDRQ#(pin 8) internal is no use



SRBM=1: Enable shared memory with host BIOS

I/O Address		
Address	Index	Data
0 0	2F	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL)(HCFGBAL, HCFGBAL)+1	
1 1	Reserved	

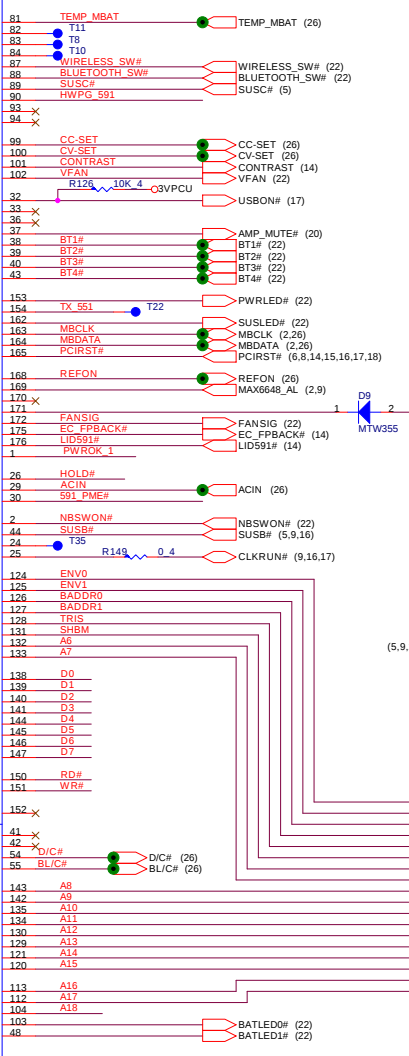
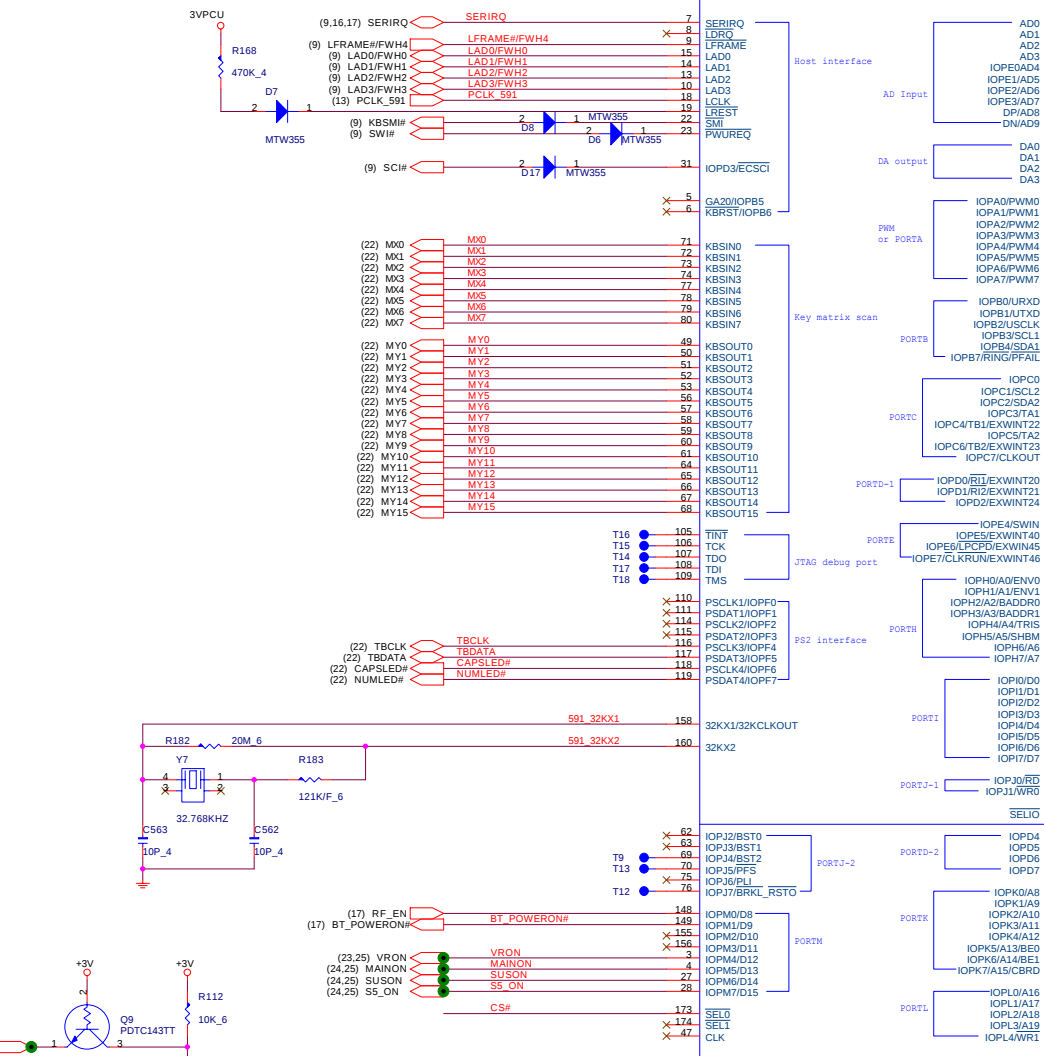


BIU configuration should match flash speed used

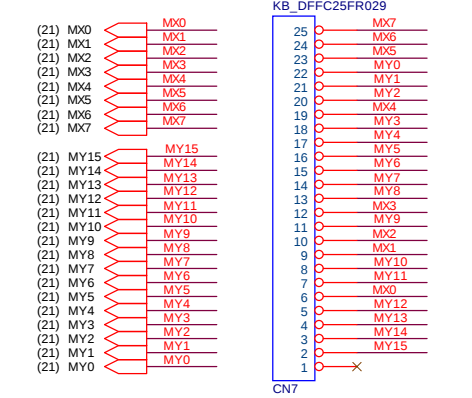


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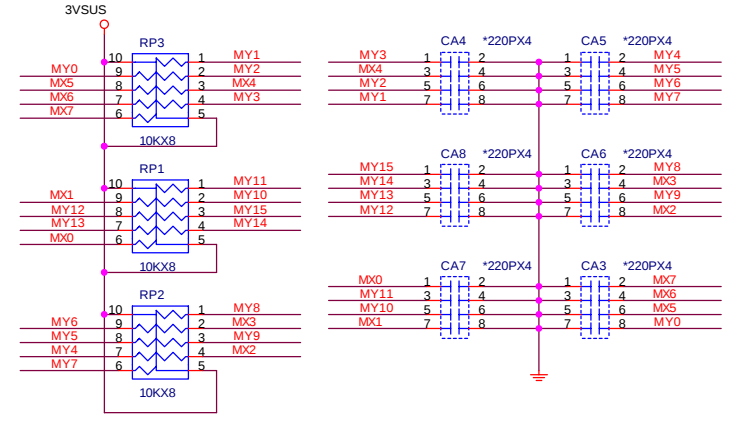
Size	Document Number	Rev
	97551 & FLASH	2A
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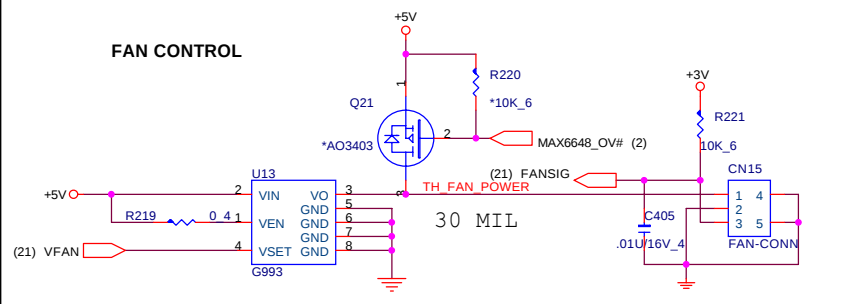
INT K/B



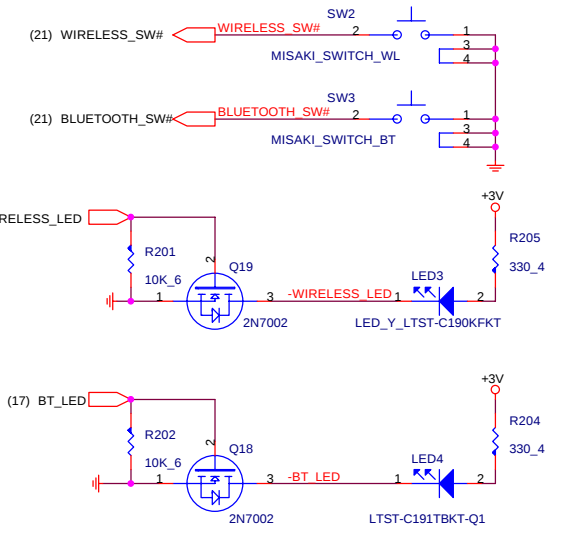
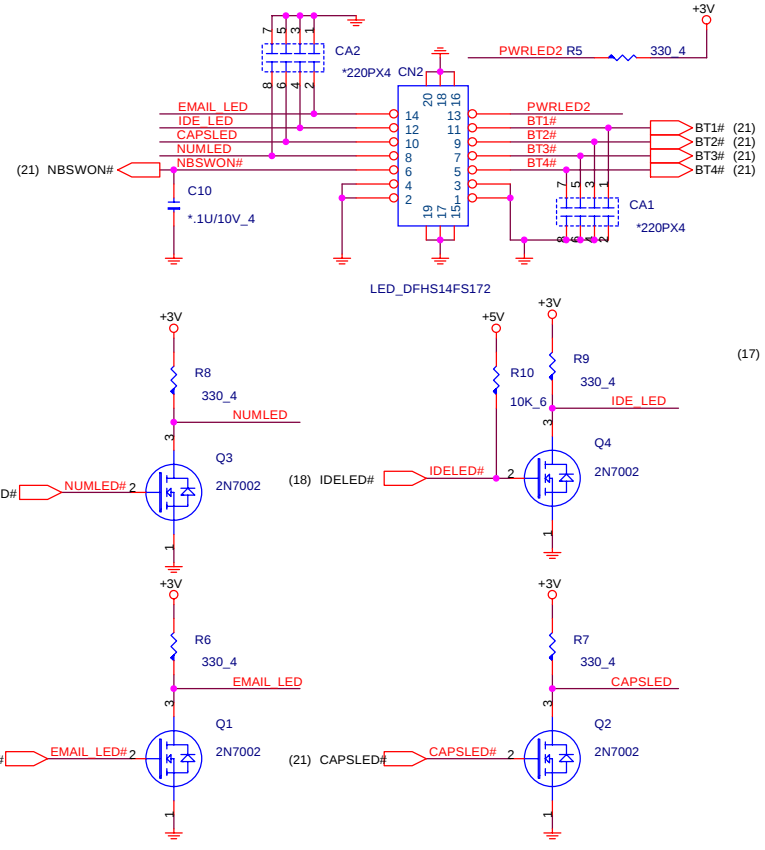
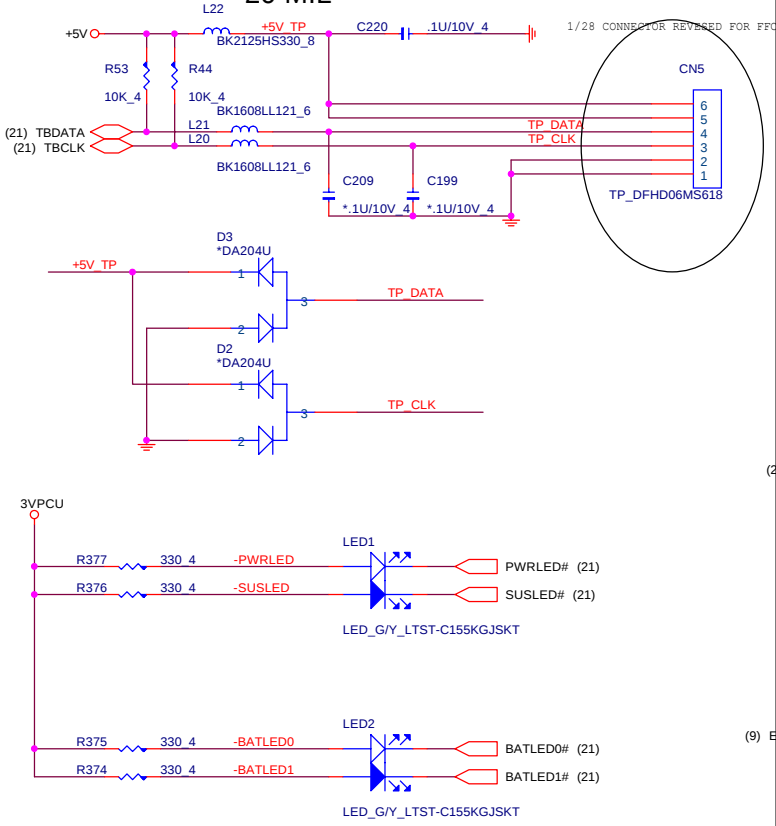
CONNECTION



FAN CONTROL



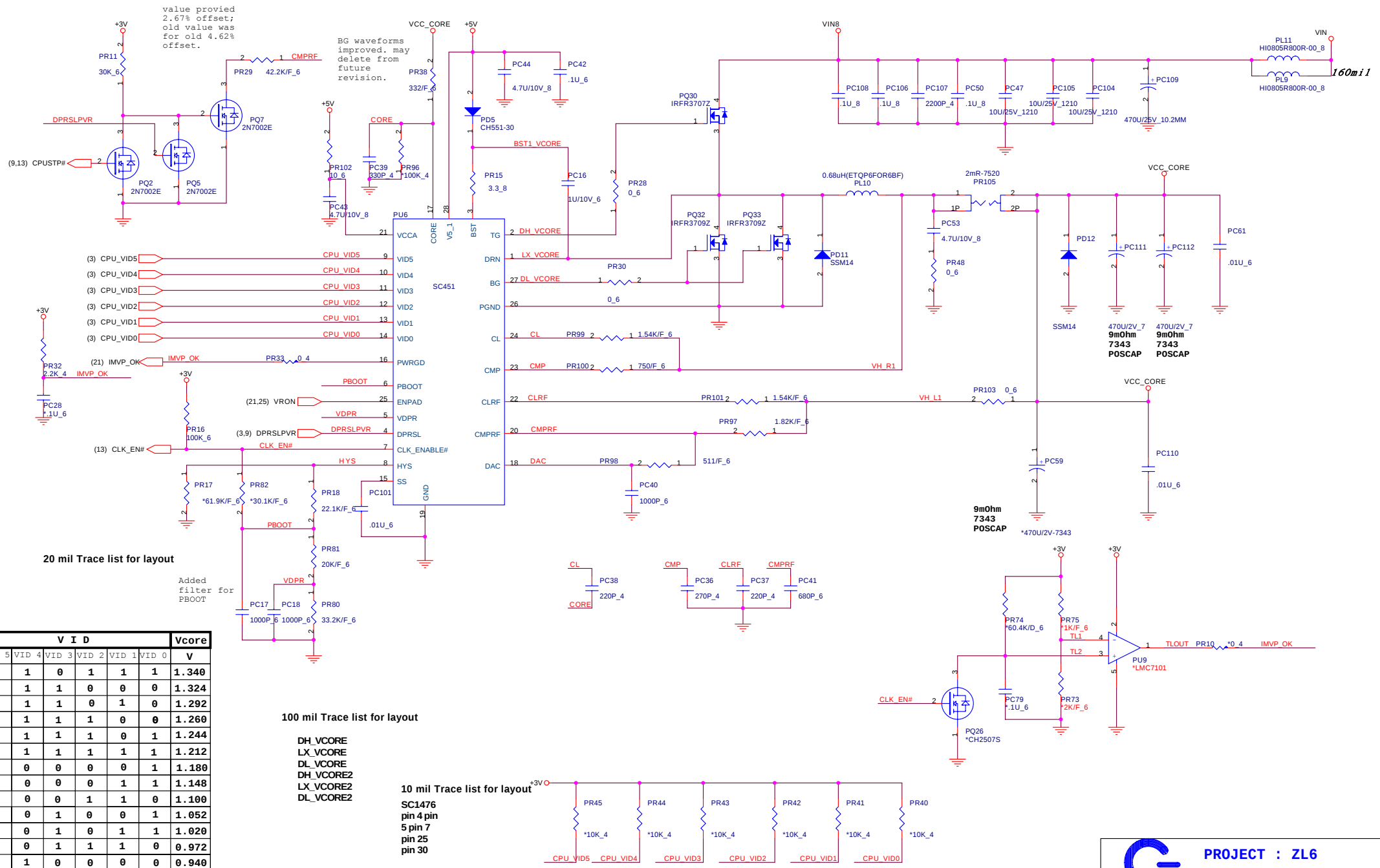
TOUCH PAD





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Size	Document Number	Rev
	T/P,FAN,SWITCH,LED,K/B	2A
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value provided
2.67% offset;
old value was
for old 4.62%
offset.

BG waveforms
improved, may
delete from
future
revision.

20 mil Trace list for layout

Added
filter
for
PBOOT

V I D							Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0	V	
0	1	0	1	1	1	1.340	
0	1	1	0	0	0	1.324	
0	1	1	0	1	0	1.292	
0	1	1	1	0	0	1.260	
0	1	1	1	1	0	1.244	
0	1	1	1	1	1	1.212	
1	0	0	0	0	1	1.180	
1	0	0	0	1	1	1.148	
1	0	0	1	1	0	1.100	
1	0	1	0	0	1	1.052	
1	0	1	0	1	1	1.020	
1	0	1	1	1	0	0.972	
1	1	0	0	0	0	0.940	

100 mil Trace list for layout

DH_VCORE
LX_VCORE
DL_VCORE
DH_VCORE2
LX_VCORE2
DL_VCORE2

10 mil Trace list for layout

SC1476
pin 4 pin
5 pin 7
pin 25
pin 30

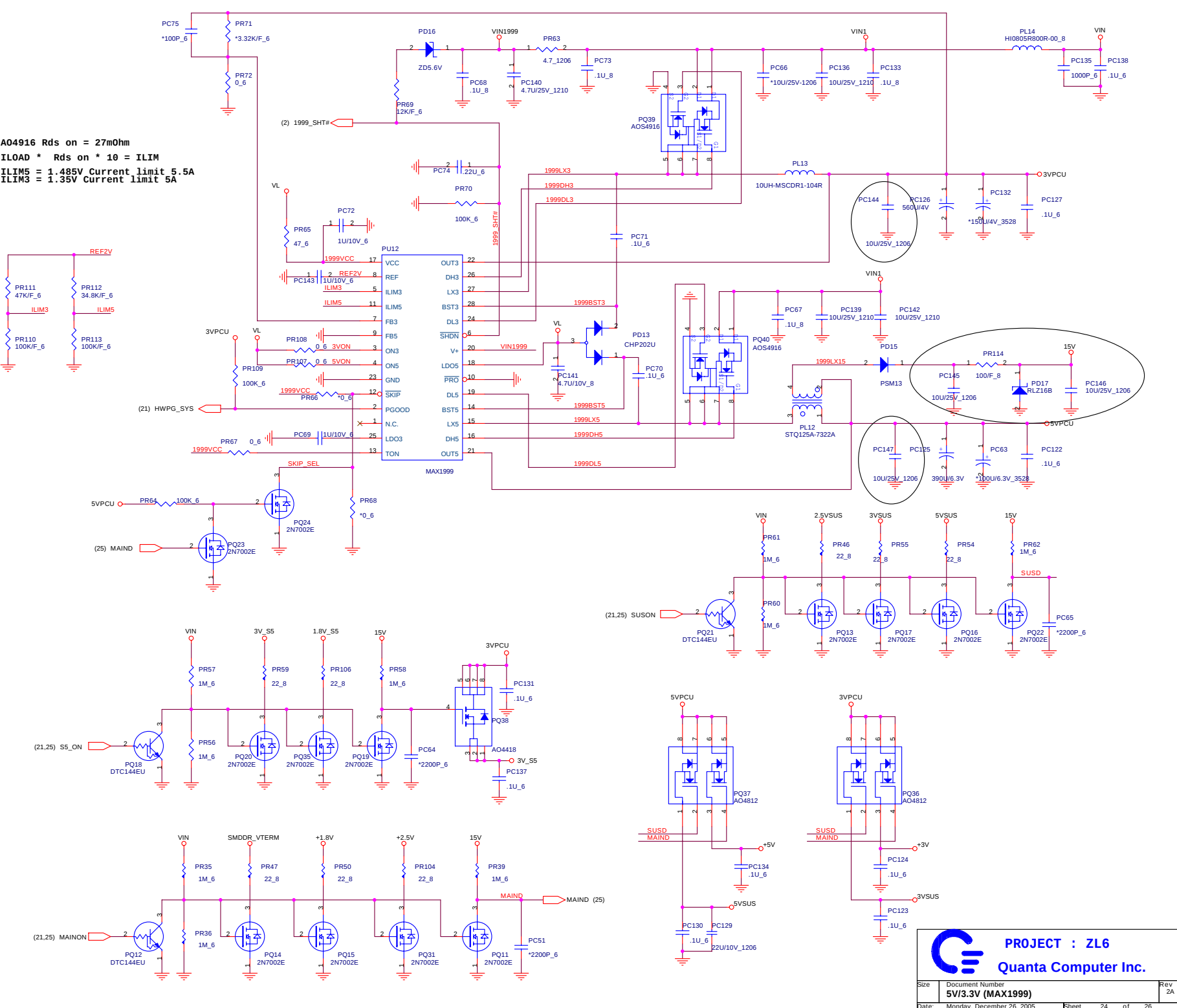


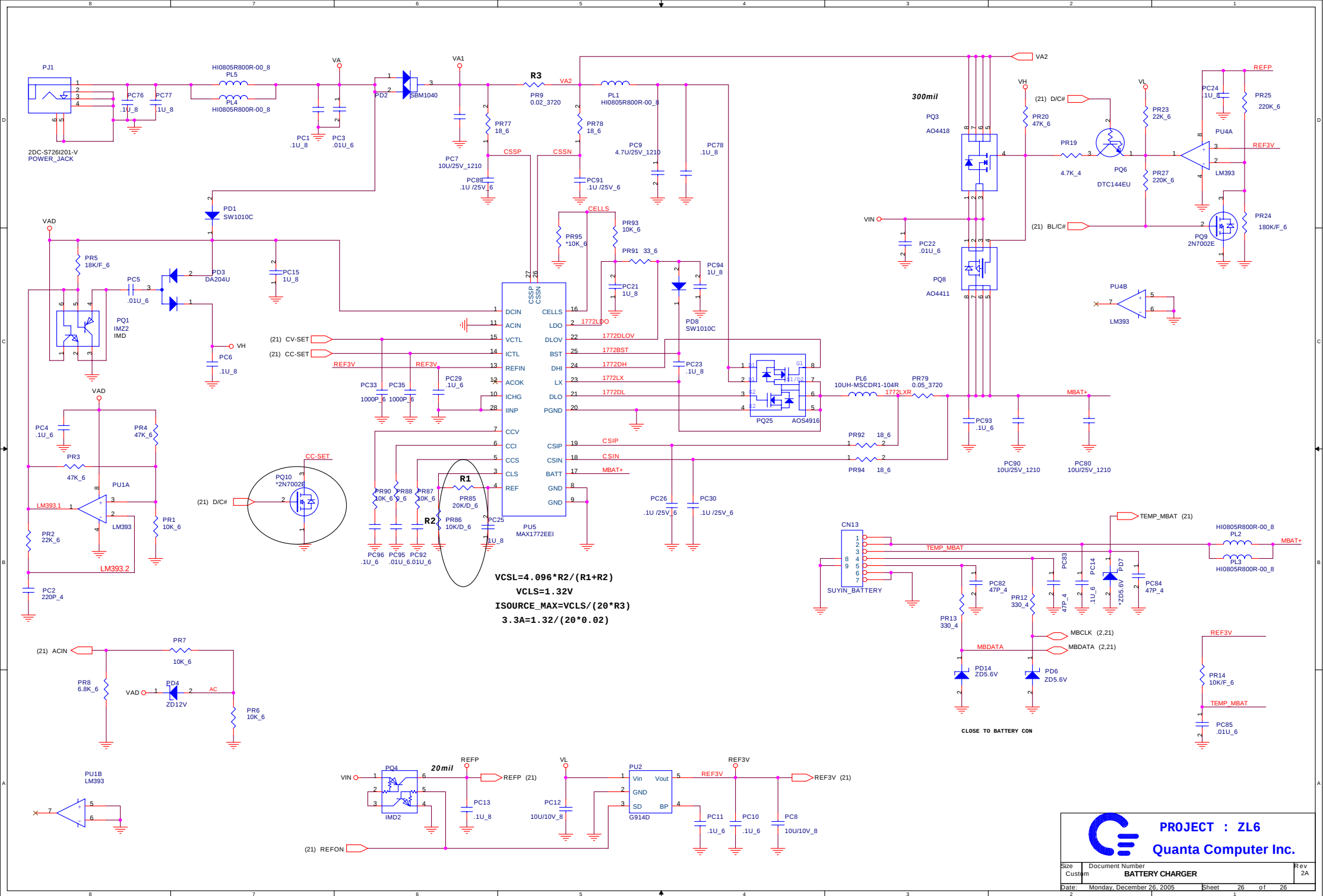
PROJECT : ZL6

Quanta Computer Inc.

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A04916 Rds on = 27mOhm
 ILOAD * Rds on * 10 = ILIM
 ILIM5 = 1.485V Current limit 5.5A
 ILIM3 = 1.35V Current limit 5A





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